



NRI INSTITUTE OF TECHNOLOGY

(Approved by AICTE, New Delhi :: Affiliated to JNTUK, Kakinada)

POTHAVARAPPADU (V), (via) Nunna, Agiripalli (M), Krishna District, A.P. PIN : 521 212 Ph : 08656-324999

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
II B. Tech - I SEMESTER – (R16 REGULATION) SUPPLE EXAMINATIONS, OCT/NOV-2020


NRIIT/7.5. FT 12


T I M E T A B L E

Time : 02.00 P.M TO 5.00 P.M

BRANCH	DATE AND DAY					
	27-10-2020 TUESDAY	29-10-2020 THURSDAY	02-11-2020 MONDAY	04-11-2020 WEDNESDAY	06-11-2020 FRIDAY	10-11-2020 TUESDAY
CIVIL	FLUID MECHANICS	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	PROBABILITY & STATISTICS	STRENGTH OF MATERIALS-I	SURVEYING	BUILDING MATERIALS AND CONSTRUCTION
EEE	ELECTRICAL CIRCUIT ANALYSIS-II	THERMAL AND HYDRO PRIME MOVIERS	BASIC ELECTRONICS AND DEVICES	ELECTRICAL MACHINES-I	ELECTRO MAGNETIC FIELDS	MEFA
MECH	METTALLURGY&MAT ERIALS SCIENCE	THERMODYNAMIC	----	MECHANICS OF SOLIDS	FLUID MECHANICS & HYDRAULIC MACHINES	MEFA
ECE	NETWORK ANALYSIS	SWITCHING THEORY AND LOGIC DESIGN	ELECTRONIC DEVICES AND CIRCUITS	RANDOM VARIABLES AND STOCHASTIC PROCESS	SIGNALS & SYSTEMS	MEFA
CSE	STATISTICS WITH R PROGRAMMING	MATHEMATICAL FOUNDATION OF COMPUTER SCIENCE AND ENGG	DATA STRUCTURES THROUGH C++	COMPUTER GRAPHICS	DIGITAL LOGIC DESIGN	PYTHON PROGRAMMING
IT	STATISTICS WITH R PROGRAMMING	MATHEMATICAL FOUNDATION OF COMPUTER SCIENCE AND ENGG	DATA STRUCTURES THROUGH C++	SOFTWARE ENGINEERING	DIGITAL LOGIC DESIGN	PYTHON PROGRAMMING

EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.


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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
II B. Tech - I SEMESTER – (R13 REGULATION) SUPPLE EXAMINATIONS, OCT/NOV-2020


NRIIT/7.5.1/FT 12


T I M E T A B L E

Time : 02.00 P.M TO 5.00 P.M

BRANCH	DATE AND DAY					
	27-10-2020 TUESDAY	29-10-2020 THURSDAY	02-11-2020 MONDAY	04-11-2020 WEDNESDAY	06-11-2020 FRIDAY	10-11-2020 TUESDAY
CIVIL	FLUID MECHANICS	BASIC ELECTRICAL & ELECTRONICS ENGG.	PROBABILITY & STATISTICS	STRENGTH OF MATERIALS - I	SURVEYING	BUILDING MATERIALS AND CONSTRUCTION
EEE	ELECTRICAL CIRCUIT ANALYSIS-II	THERMAL AND HYDRO PRIME MOVERS	BASIC ELECTRONICS AND DEVICES	COMPLEX VARIABLES AND STATISTICAL METHODS	ELECTRO MAGNETIC FIELDS	ELECTRICAL MACHINES-I
MECH	METALLURGY & MATERIALS SCIENCE	BASIC ELECTRICAL & ELECTRONICS ENGG.	THERMO DYNAMICS	MEFA	MECHANICS OF SOLIDS	---
ECE	DATA STRUCTURES	ELECTRICAL TECHNOLOGY	ELECTRONIC DEVICES AND CIRCUITS	MEFA	SIGNALS & SYSTEMS	ENVIRONMENTAL STUDIES
CSE	DATA STRUCTURES	MFCS	OBJECT ORIENTED PROGRAMMING THROUGH C++	MEFA	DIGITAL LOGIC DESIGN	---
IT	DATA STRUCTURES	MFCS	OBJECT ORIENTED PROGRAMMING THROUGH C++	MEFA	DIGITAL LOGIC DESIGN	---

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 POTHAVARAPPADU (V), (VIA) NUNNA, AGIRIPALLI (M), PIN – 521 212

II B. Tech - I SEMESTER – (R10 REGULATION) SUPPLE EXAMINATIONS, OCT/NOV-2020 TIME TABLE

Time : 02.00 P.M TO 5.00 P.M

BRANCH	DATE AND DAY					
	27-10-2020 TUESDAY	29-10-2020 THURSDAY	02-11-2020 MONDAY	04-11-2020 WEDNESDAY	06-11-2020 FRIDAY	10-11-2020 TUESDAY
CIVIL	MECHANICS OF MATERIALS	CONSTRUCTION MATERIALS AND MANAGEMENT	EEE	MATHEMATICS-III	SURVEYING	FLUID MECHANICS
EEE	FM & HM	MEFA	ELECTRICAL CIRCUIT ANALYSIS – I	ELECTRO MAGNETIC FIELD	ELECTRICAL MACHINES – I	ELECTRONIC DEVICES AND CIRCUITS
MECH	FM & HM	MEFA	EEE	ENGINEERING MECHANICS	THERMO DYNAMICS	-----
ECE	ELECTRICAL TECHNOLOGY	MEFA	NETWORK ANALYSIS	PTSP	SIGNALS & SYSTEMS	ELECTRONIC DEVICES AND CIRCUITS
CSE	DATA STRUCTURES	MEFA	PROBABILITY & STATISTICS	MFCS	DIGITAL LOGIC DESIGN	ELECTRONIC DEVICES AND CIRCUITS
IT	DATA STRUCTURES	MEFA	PROBABILITY & STATISTICS	MFCS	DIGITAL LOGIC DESIGN	ELECTRONIC DEVICES AND CIRCUITS

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